



5-15-02  
PB

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 10/050,348  
Filing Date ..... January 15, 2002  
Inventor ..... Gurtej S. Sandhu et al.  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... Unknown  
Examiner ..... Unknown  
Attorney's Docket No. .... MI22-1898  
Title: Transistor Structures

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References –See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing date of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925.

Dated: \_\_\_\_\_

3/12/02

Respectfully submitted,

By: \_\_\_\_\_

David G. Latwesen, Ph.D.  
Reg. No. 38,533

**EV 026156929**